

L Number	Hits	Search Text	DB	Time stamp
-	330	712/228.ccls. and @ad<20001229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 14:07
-	72	(712/228.ccls. and @ad<20001229) and thread	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 07:52
-	251	(thread and (branch near predict\$4)) and @ad<20001229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/24 08:19
-	175	((thread and (branch near predict\$4)) and @ad<20001229) and latency	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:27
-	158	((((thread and (branch near predict\$4)) and @ad<20001229) and latency) not ((712/228.ccls. and @ad<20001229) and thread)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:27
-	12	"multi-threading processor" and @ad<20001229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 14:07
-	3797	thread and switch\$4 and branch and @ad<20001229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/24 10:56
-	97	thread same switch\$4 same branch and @ad<20001229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/24 10:54
-	1151	(branch near2 mispredict\$4) and @ad<20001229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/24 10:56
-	455	((branch near2 mispredict\$4)and latency) and @ad<20001229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/24 11:00
-	67	((branch near2 mispredict\$4) same latency) and @ad<20001229	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/24 13:16